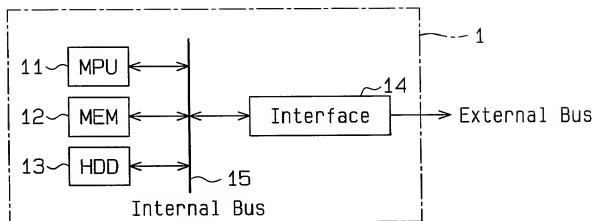
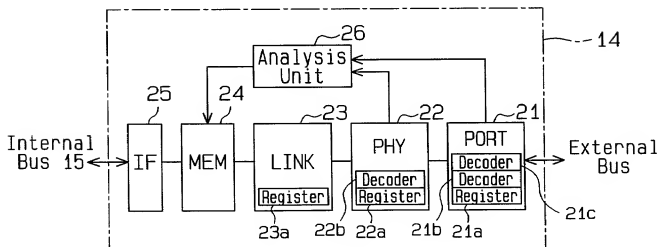


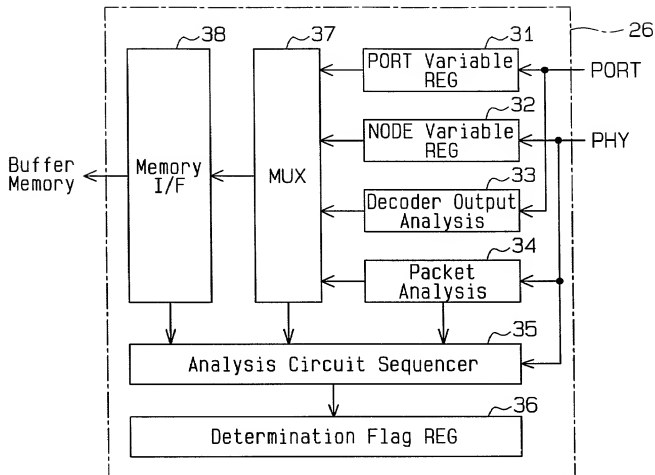
# Fig.2



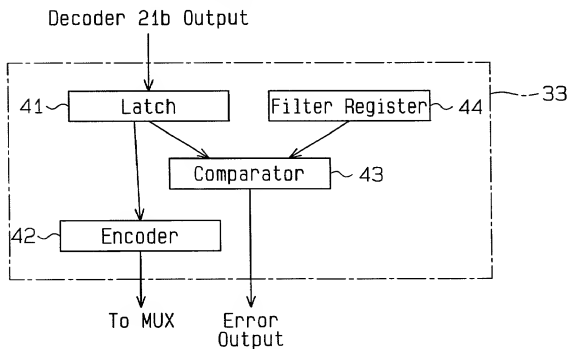
# Fig.3



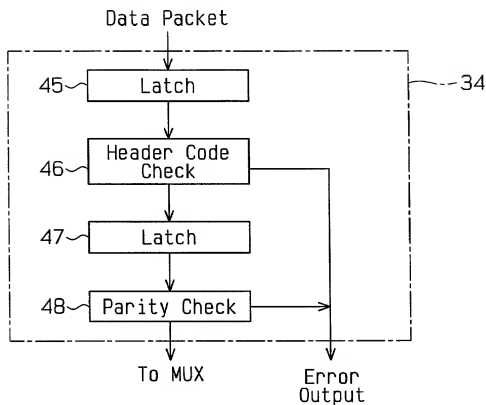
**Fig.4**



**Fig.5**



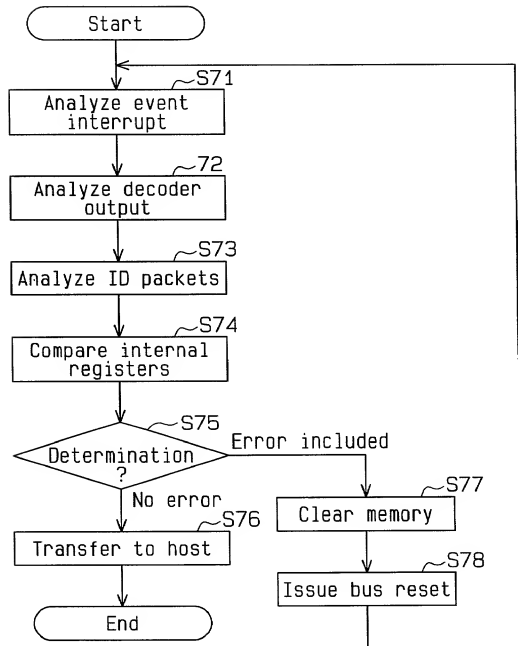
**Fig.6**



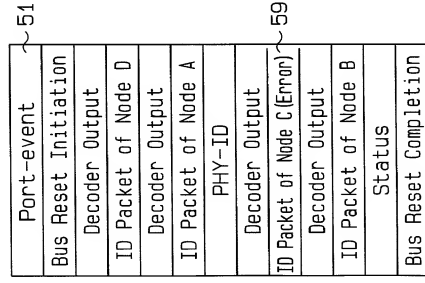
**Fig.7**

Port-event	50
Bus Reset Initiation	51
Decoder Output	52
ID Packet of Node D	53
Decoder Output	54
ID Packet of Node A	55
PHY-ID	56
Decoder Output	57
ID Packet of Node C	58
Decoder Output	59
ID Packet of Node B	60
Status	61
Bus Reset Completion	62
	63

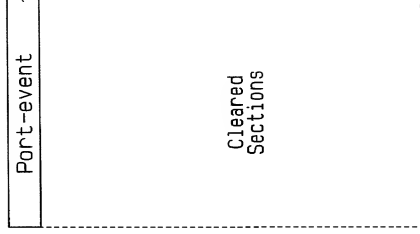
**Fig.8**



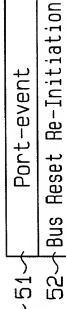
**Fig. 9(a)**



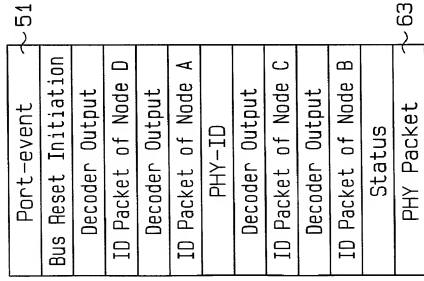
**Fig. 9(b)**



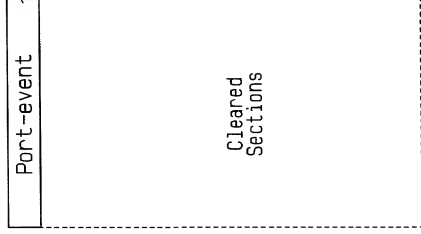
**Fig. 9(c)**



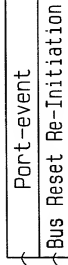
**Fig.10(a)**



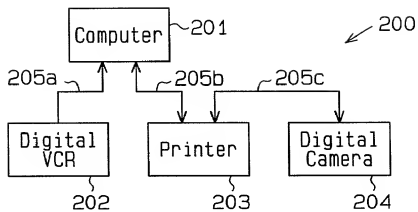
**Fig.10(b)**



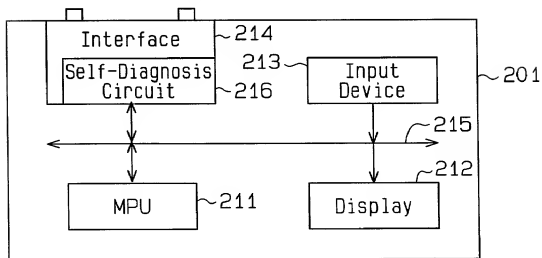
**Fig.10(c)**



**Fig.11**

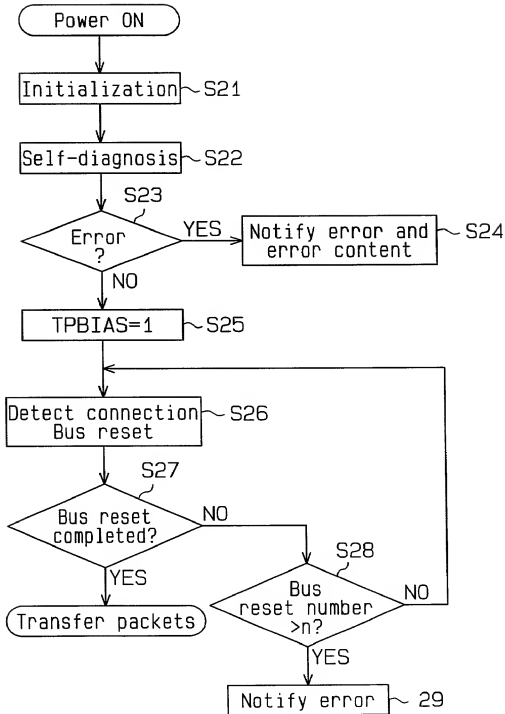


**Fig.12**

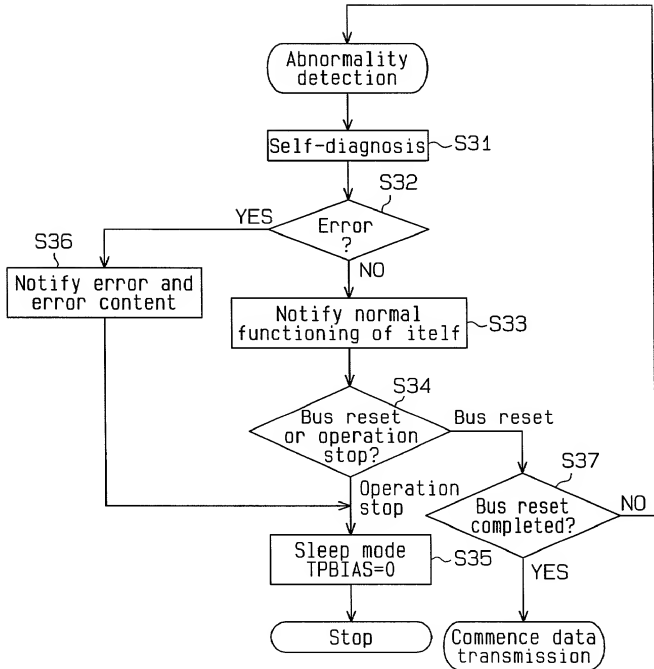




**Fig.13**



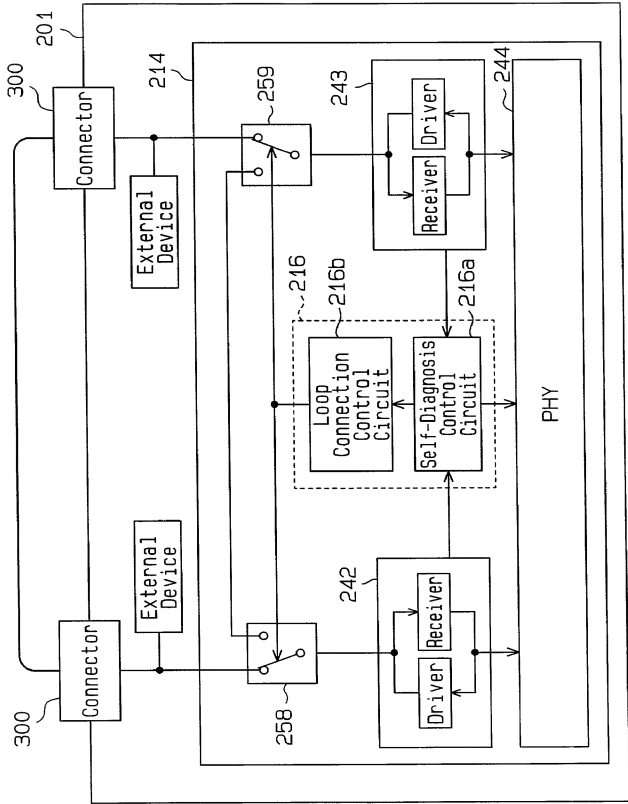
**Fig.14**



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00512140745760



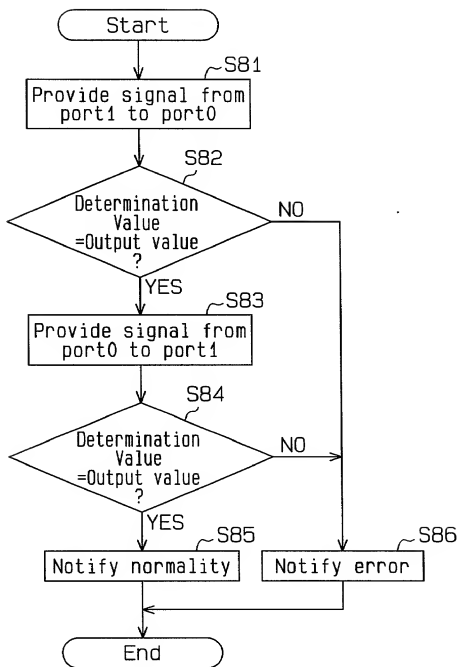
**Fig.16**





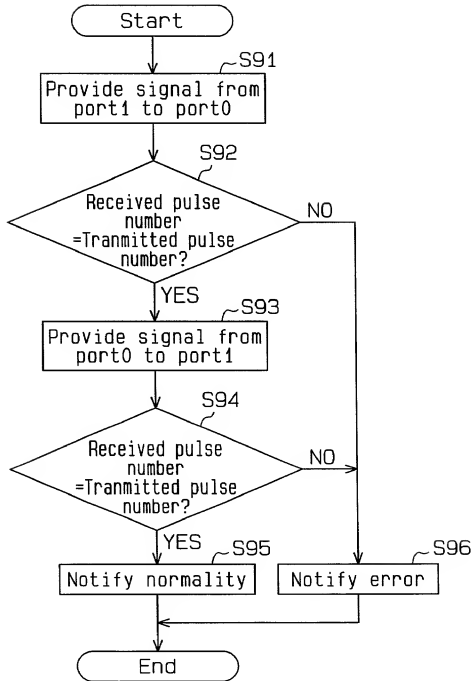


**Fig.19**



09734704.13360

**Fig.20**





**Fig.21**

